ECE 325
Spring 2017 Lab Policy

Section 505: W 08:00 AM - 10:50 AM (CVLB 423, Noah)
Section 506: W 11:30 AM-2:20 PM (CVLB 423, Noah)
Section 507: W 03:00 PM - 05:50 PM (CVLB 423, Noah)
Section 511: Th 05:30 PM - 08:20 PM (CVLB 423, Ashkan)

Recitation: F 09:10 PM – 10:10 PM (CHEN 104, Noah)
- Attendance is MANDATORY. There will be pop-quizzes at random
times (up to 5% of extra lab grade)

TA: Noah Hae-woong Yang
Office: WERC 015F
Office Hours: T 12-2pm
Email: hwyang@tamu.edu

TA: Ashkan Roshan Zamir
Office: WERC 315A
Office Hours: M 3-5pm
Email: ashkanroshan@tamu.edu

Grading:
  Pre-labs 40%
  - One report per person
  - Due at the beginning of the lab
  - No late pre-labs will be accepted without any university approved excuse

  Lab Reports 60%
  - One report per group of two or one person
  - Due at the beginning of the following lab session, -0.5dB/hour penalty
    from the score you get.

Policies
  - Pre-labs may be hand written but they need to be legible and understandable.
  - Pre-labs must be:
    ➢ Clean sheet of paper (do not use paper that has been previously used for
      printing unrelated text)
    ➢ Clear handwriting (consider typing the text part of your pre-labs)
    ➢ Follow the same components labeling as in the lab manual
    ➢ Numbered equations
    ➢ Boxed final results
  - Reports must be TYPED, please do not cut-and-paste from the lab manual. Make
    sure all plots/schematics have LIGHT background by inverting color if necessary.
  - Plots/Pictures need to have captions, axis names, marks and annotations if needed.
  - No food or drink in the lab.
ECEN 325 Lab Schedule, Spring 2017

Location: CVLB 423  
TA: Noah Hae-woong Yang (hwyang@tamu.edu)  
Office Hours: Tuesday 12-2PM

<table>
<thead>
<tr>
<th>Lab No.</th>
<th>Lab Time</th>
<th>Report Due</th>
<th>Topic</th>
</tr>
</thead>
</table>
| 1       | Sec.505 2/1 W 8-10:50am  
Sec.506 2/1 W 11:30am-2:20pm  
Sec.507 2/1 W 3-5:50pm | Beginning of next lab | Network Analysis and Bode plots |
| 2       | Sec.505 2/8 W 8-10:50am  
Sec.506 2/8 W 11:30am-2:20pm  
Sec.507 2/8 W 3-5:50pm | Beginning of next lab | Introduction to NI Elvis Environment |
| 3       | Sec.505 2/15 W 8-10:50am  
Sec.506 2/15 W 11:30am-2:20pm  
Sec.507 2/15 W 3-5:50pm | Beginning of next lab | Operational Amplifiers-Part I |
| 4       | Sec.505 2/22 W 8-10:50am  
Sec.506 2/22 W 11:30am-2:20pm  
Sec.507 2/22 W 3-5:50pm | Beginning of next lab | Operational Amplifiers-Part II |
| 5       | Sec.505 3/1 W 8-10:50am  
Sec.506 3/1 W 11:30am-2:20pm  
Sec.507 3/1 W 3-5:50pm | Beginning of next lab | Operational Amplifiers-Part III |
| 6       | Sec.505 3/8 W 8-10:50am  
Sec.506 3/8 W 11:30am-2:20pm  
Sec.507 3/8 W 3-5:50pm | Beginning of next lab | Introduction to Diodes |
| 7       | Sec.505 3/22 W 8-10:50am  
Sec.506 3/22 W 11:30am-2:20pm  
Sec.507 3/22 W 3-5:50pm | Beginning of next lab | Characterization of the BJT |
| 8       | Sec.505 3/29 W 8-10:50am  
Sec.506 3/29 W 11:30am-2:20pm  
Sec.507 3/29 W 3-5:50pm | Beginning of next lab | BJT Amplifiers: Basic configurations |
| 9       | Sec.505 4/5 W 8-10:50am  
Sec.506 4/5 W 11:30am-2:20pm  
Sec.507 4/5 W 3-5:50pm | Beginning of next lab | BJT amplifiers: Design project |
| 10      | Sec.505 4/12 W 8-10:50am  
Sec.506 4/12 W 11:30am-2:20pm  
Sec.507 4/12 W 3-5:50pm | Beginning of next lab | Characterization of the MOS transistor |
| 11      | Sec.505 4/19 W 8-10:50am  
Sec.506 4/19 W 11:30am-2:20pm  
Sec.507 4/19 W 3-5:50pm | Apr 26 | CMOS amplifier configurations |
| Final Design Project | Project Report due: TBA  
TAs will attend the normal lab time to help you with final project for reduced amount of time |
**ECEN 325 Lab Schedule, Spring 2017**

Location: CVLB 423  
**TA:** Ashakn Roshan Zamir (Ashkanroshan@tamu.edu)  
Office Hours: M 3-5pm

<table>
<thead>
<tr>
<th>Lab No.</th>
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<th>Report Due</th>
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<tbody>
<tr>
<td>1</td>
<td>Sec.511 2/2 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>Network Analysis and Bode plots</td>
</tr>
<tr>
<td>2</td>
<td>Sec.511 2/9 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>Introduction to NI Elvis Environment</td>
</tr>
<tr>
<td>3</td>
<td>Sec.511 2/16 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>Operational Amplifiers-Part I</td>
</tr>
<tr>
<td>4</td>
<td>Sec.511 2/23 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>Operational Amplifiers-Part II</td>
</tr>
<tr>
<td>5</td>
<td>Sec.511 3/2 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>Operational Amplifiers-Part III</td>
</tr>
<tr>
<td>6</td>
<td>Sec.511 3/9 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>Introduction to Diodes</td>
</tr>
<tr>
<td>7</td>
<td>Sec.511 3/23 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>Characterization of the BJT</td>
</tr>
<tr>
<td>8</td>
<td>Sec.511 3/30 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>BJT Amplifiers: Basic configurations</td>
</tr>
<tr>
<td>9</td>
<td>Sec.511 4/6 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>BJT amplifiers: Design project</td>
</tr>
<tr>
<td>10</td>
<td>Sec.511 4/13 Th 5:30-8:20pm</td>
<td>Beginning of next lab</td>
<td>Characterization of the MOS transistor</td>
</tr>
<tr>
<td>11</td>
<td>Sec.511 4/20 Th 5:30-8:20pm</td>
<td>Apr 27</td>
<td>CMOS amplifier configurations</td>
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**Final Design Project**  
Project Report due: TBA  
TAs will attend the normal lab time to help you with final project for reduced amount of time