

**Texas A&M University
Department of Electrical and Computer Engineering**

ECEN 325 – Electronics

Spring 2020

Exam #2

Instructor: Sam Palermo

- Please write your name in the space provided below
- Please verify that there are **6** pages in your exam
- Good Luck!

Problem	Score	Max Score
1		30
2		35
3		35
Total		100

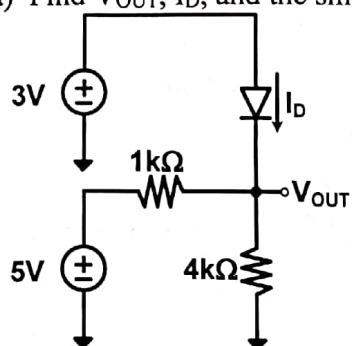
Name: SAM PALERMO

UIN: _____

Problem 1 (30 points)

For all the circuits below, use the constant-voltage-drop diode model ($V_D=0.7V$), $V_T=25.9mV$, and $n=1$.

- a) Find V_{OUT} , I_D , and the small signal diode resistance, r_d . (10 points)



If diode is reverse biased

$$V_{D,UT} = 4V$$

$$V_D = -1V \text{ and } I_D = 0$$

\Rightarrow Consistent w/ diode model

$$r_d = \frac{V_T}{I_D} = \frac{25.9mV}{0} = \infty$$

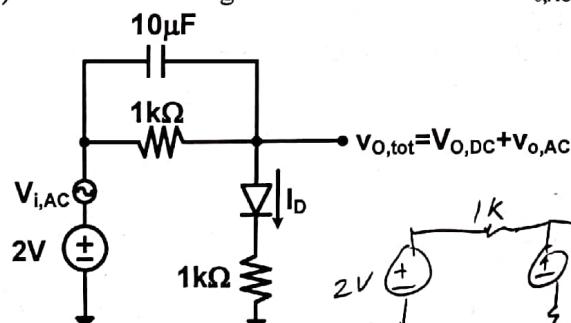
$$V_{OUT} = 4V$$

$$I_D = 0$$

$$r_d = \infty$$

The following circuit has a small-signal AC signal, $v_{i,AC}$, in series with a DC voltage.

- b) Find the DC output voltage, $V_{O,DC}$, DC diode current I_D , and small-signal diode r_d (10 points)
 c) Find the small-signal AC transfer function $v_o(s)/v_{i,AC}(s)$ (10 points)



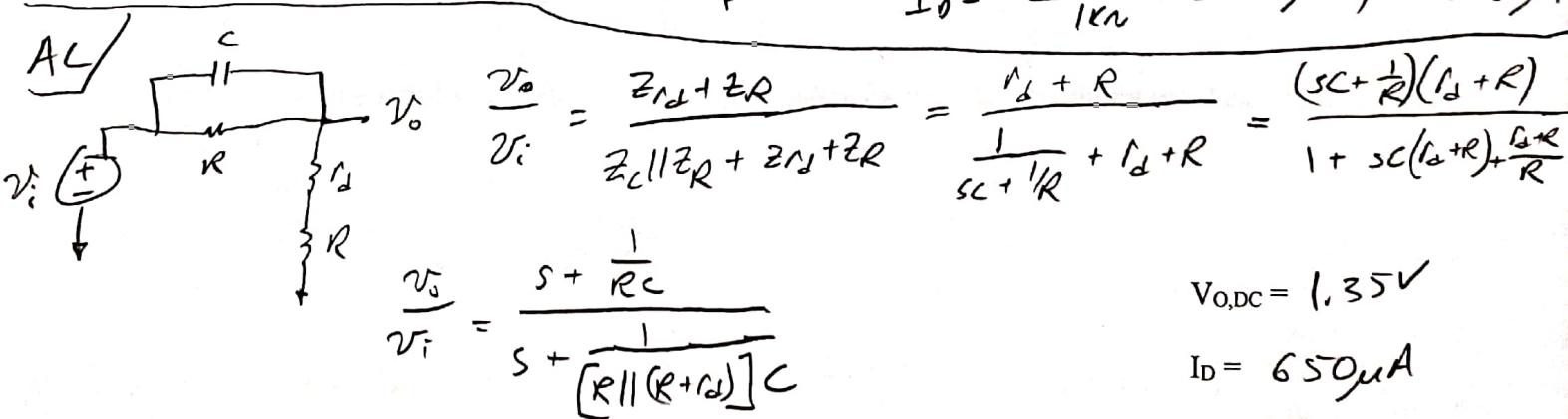
DC If diode is reverse biased,

$$V_{D,DC} = V_b = 2V \Rightarrow \text{Not consistent}$$

diode must be forward biased

$$\frac{V_{O,DC} - 2V}{1k} + \frac{V_{O,UT} - 0.7V}{1k} = 0 \Rightarrow V_{O,UT} = 1.35V$$

$$I_D = \frac{1.35V - 0.7V}{1k\Omega} = 650\mu A, r_d = \frac{V_T}{I_D} = \frac{25.9mV}{650\mu A}$$



$$\frac{V_o}{V_i} = \frac{Z_{D,I} + Z_R}{Z_C || Z_R + Z_{D,I} + Z_R} = \frac{r_d + R}{\frac{1}{SC} + \frac{1}{R} + r_d + R} = \frac{(SC + \frac{1}{R})(r_d + R)}{1 + SC(r_d + R) + \frac{1}{R}}$$

$$V_{O,DC} = 1.35V$$

$$I_D = 650\mu A$$

$$r_d = 39.8\Omega$$

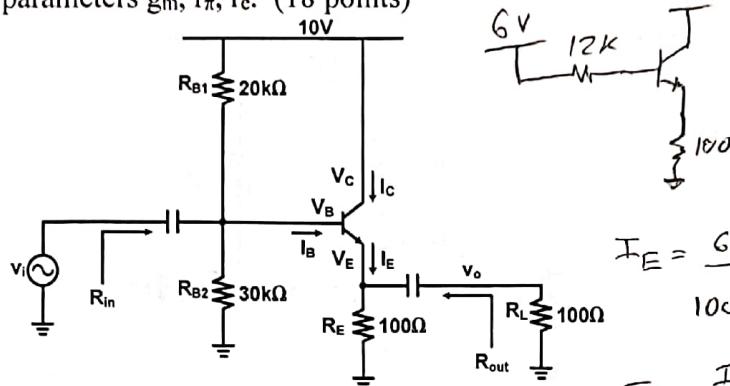
AC Transfer Function:

$$\frac{s + 100}{s + 196}$$

Problem 2 (35 points)

Assume for problem 2 that the transistor $\beta=150$, $V_{BE}=0.7V$, and $V_T=25.9mV$.

- a) Calculate the DC values for V_C , V_B , V_E , I_C , I_B , and I_E . Compute the AC small signal parameters g_m , r_π , r_e . (18 points)



$$V_E = 29.5mA (100\Omega) = 2.95V$$

$$V_B = V_E + 0.7V = 3.65V$$

$$g_m = \frac{I_C}{V_T} = \frac{29.3mA}{25.9mV} = 1.13 A/V$$

$$r_\pi = \frac{V_T}{I_B} = \frac{25.9mV}{196\mu A} = 132\Omega$$

$$r_e = \frac{V_T}{I_E} = \frac{25.9mV}{29.5mA} = 0.88\Omega$$

$$I_C = 29.3mA$$

$$I_B = 196\mu A$$

$$I_E = 29.5mA$$

$$V_C = 10V$$

$$V_B = 3.65V$$

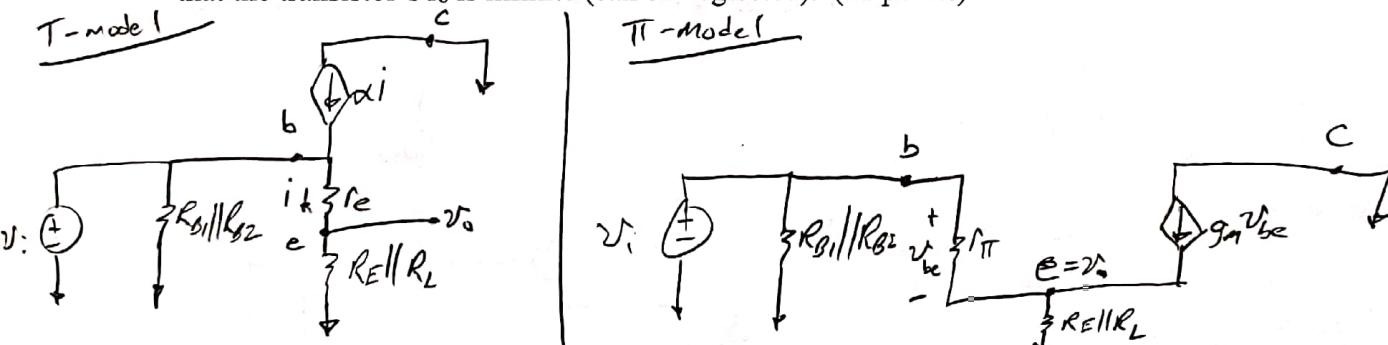
$$V_E = 2.95V$$

$$g_m = 1.13 A/V$$

$$r_\pi = 132\Omega$$

$$r_e = 0.88\Omega$$

- b) Sketch the small-signal model of the circuit. Assume that the capacitors act as AC shorts and that the transistor's r_0 is infinite (can be neglected). (11 points)



- c) Calculate the small signal gain $A_v = v_o/v_i$, the input resistance R_{in} , the output resistance R_{out} . (6 points)

$$A_v = \frac{R_E || R_L}{r_e + R_E || R_L} = \frac{100 || 100}{0.88 + 100 || 100} = 0.983$$

$$\begin{aligned} R_{in} &= R_B \parallel [r_\pi + (\beta+1)(R_E || R_L)] \\ &= 12k\Omega \parallel [132 + 151(100 || 100)] \\ &= 4.68k\Omega \end{aligned}$$

$$\begin{aligned} R_{out} &= R_E \parallel r_e \parallel \frac{R_L}{\beta+1} \\ &= 100 \parallel 0.88 \\ &= 0.87\Omega \end{aligned}$$

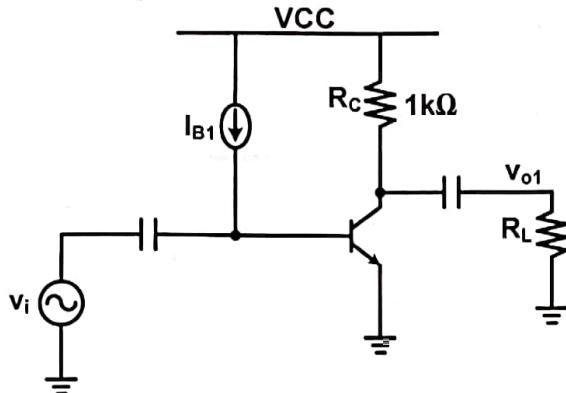
$$A_v = 0.983$$

$$R_{in} = 4.68k\Omega$$

$$R_{out} = 0.87\Omega$$

Problem 3 (35 points)

- a) For the following amplifier, assume the transistor $\beta=150$, $r_o=\infty$, and that the transistor is biased such that $g_m=40\text{mA/V}$, $r_e=24.8\Omega$, and $r_\pi=3.8\text{k}\Omega$. Calculate the amplifier gain, $A_{v1}=v_{o1}/v_i$, for two load resistor conditions: $R_L=\infty$ and $R_L=16\Omega$. Recall that an ideal current source has infinite output resistance, ie $R_B=\infty$. Assume that the capacitors act as AC shorts. (6 points)



$$A_v = -g_m (R_c \parallel R_L)$$

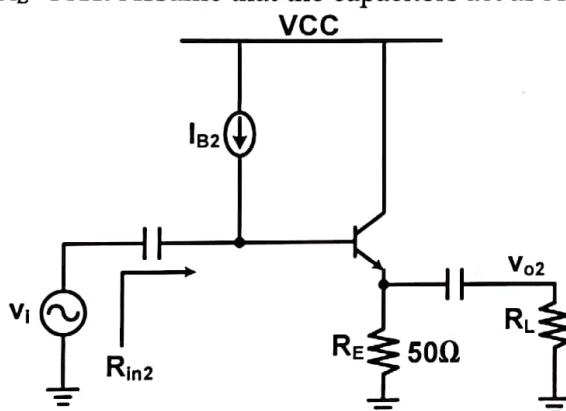
$$\text{w/ } R_L = \infty \Rightarrow -40 \text{mA/V} (1k) = -40 \text{V/V}$$

$$\text{w/ } R_L = 16 \Rightarrow -40 \text{mA/V} (1k \parallel 16) = -0.63 \text{V/V}$$

$$A_{v1} (R_L=\infty) = -40$$

$$A_{v1} (R_L=16\Omega) = -0.63$$

- b) For the following amplifier, assume the same transistor bias conditions: $\beta=150$, $r_o=\infty$, and that the transistor is biased such that $g_m=1.93\text{A/V}$, $r_e=0.51\Omega$, and $r_\pi=78\Omega$. Calculate the amplifier gain, $A_{v2}=v_{o2}/v_i$, and the input resistance, R_{in2} , for two load resistor conditions: $R_L=\infty$ and $R_L=16\Omega$. Assume that the capacitors act as AC shorts. (14 points)



$$A_v = \frac{R_E \parallel R_L}{r_e + R_E \parallel R_L}$$

$$\text{w/ } R_L = \infty \Rightarrow \frac{50}{0.51 + 50} = 0.99 \text{V/V}$$

$$\text{w/ } R_L = 16 \Rightarrow \frac{50 \parallel 16}{0.51 + 50 \parallel 16} = 0.96 \text{V/V}$$

$$A_{v2} (R_L=\infty) = 0.99$$

$$A_{v2} (R_L=16\Omega) = 0.96$$

$$R_{in2} (R_L=\infty) = 7.63\text{k}\Omega$$

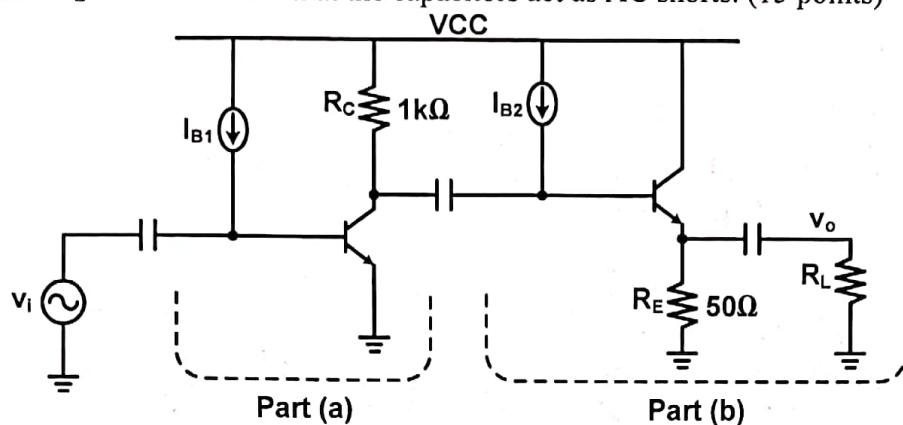
$$R_{in2} (R_L=16\Omega) = 1.91\text{k}\Omega$$

$$R_{in} = r_\pi + (\beta+1)(R_E \parallel R_c)$$

$$\text{w/ } R_L = \infty \Rightarrow R_{in} = 78 + (151)(50) = 7.63\text{k}\Omega$$

$$\text{w/ } R_L = 16 \Rightarrow R_{in} = 78 + (151)(50 \parallel 16) = 1.91\text{k}\Omega$$

- c) The circuit below consists of the amplifier from part (a) cascaded with the amplifier from part (b). Calculate the total 2-stage amplifier gain, $A_v = v_o/v_i$, for two load resistor conditions: $R_L = \infty$ and $R_L = 16\Omega$. Assume that the capacitors act as AC shorts. (15 points)



$$A_v (R_L = \infty) = -35.0$$

$$A_v (R_L = 16\Omega) = -25.2$$

$$\text{Total } A_v = (-g_m) (R_C || R_{in2}) \left(\frac{R_E || R_L}{r_e + R_E || R_L} \right)$$

$$= -g_m 1 (R_C || R_{in2}) A_{v2}$$

$$w/R_{L=\infty} \Rightarrow -40mA/v (1k || 7.63k) (0.99) = -35.0\%$$

$$w/R_{L=16} \Rightarrow -40mA/v (1k || 1.91k) (0.96) = -25.2\%$$