Texas A&M University Department of Electrical and Computer Engineering

ECEN 474/704 - (Analog) VLSI Circuit Design

Fall 2016

Exam #2

Instructor: Sam Palermo

- Please write your name in the space provided below
- Please verify that there are 5 pages in your exam
- You may use one double-sided page of notes and equations for the exam
- Good Luck!

Problem	Score	Max Score
1		30
2		30
3		40
Total		100

Name:	SAM	PALERMO	
UIN:			

Problem 1 (30 points)

For the following current source obtain the following:

- a) Give an expression for the output resistance. You can assume that all transistors are operating in saturation and that you can neglect body effect.
- b) Give an expression for the compliance voltage at the output necessary to keep all transistors in saturation.
- c) Give an expression for the minimum resistor R value required to keep all the transistors in saturation for this minimum compliance voltage. Express this minimum R value as a function of I_{in} and the relevant transistor aspect ratio (W/L) values.

Minimum Compliance Voltage

Vost = VosaT2 + VosaT4

2) Minimum VG3 = VASATI + VGS3 = VDSATI + VOSAT3+ 4

Min VE3-VE1 = VOSATI + VOSAT3 + V7 - (VASATI + 4) = Iin R

$$R = \sqrt{\frac{2}{T_{in}\mu(o\kappa(\frac{w}{\epsilon})_3)}}$$

Problem 2 (30 points)

For the fully differential amplifier below obtain the following:

- a) Give an expression for the differential gain, $A_{vd}=(v_0^+ v_0^-)/(v_i^+ v_i^-)$. Do NOT neglect the transistors' ro. Assume all transistors are operating in saturation and that you can neglect body effect.
- b) Give an expression for the dominant pole of the amplifier. This expression should include all the appropriate transistor capacitances and include the Miller effect when appropriate.

Note neglecting Miller in (401 because 1-Apa =
$$1 - (-\frac{1}{Avd})$$

wery small

c) Now simplify the differential gain expression by letting the transistors' $\mathbf{r}_0 = \infty$. Assume that the transistors have the following transistor sizes:

M1 Size = W/L

M2 Size = $\alpha*W/L$

What should
$$\alpha$$
 be to achieve a differential gain of 2?

$$AVd = \frac{g_{m_1}}{g_{m_2}} = \frac{\sqrt{\mu_1(\alpha_1 + 2(\frac{T_0}{2}))}}{\sqrt{\mu_1(\alpha_1 + 2(\frac{T_0}{2}))}}$$

$$AVd = \frac{1}{\sqrt{\alpha_1}} = \frac{1}{\sqrt{\alpha_1}} = \frac{1}{\sqrt{\alpha_1}}$$

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Problem 3 (40 points)

For the amplifier below, assume all transistors are operating in saturation and that you can neglect body effect. Obtain expressions for the following:

- a) Small-signal transconductance.
- b) Output resistance.
- c) DC gain.
- d) The amplifier's three poles. Note, it's OK here to state this as a function of an effective capacitance at a certain node, but make sure to appropriately label the nodes.
- e) Output referred noise current power spectral density. Consider only thermal noise and include all important noise sources.
- f) Input referred noise voltage power spectral density. Consider only thermal noise and include all important noise sources.

$$d. \ \omega_{p_1} = \frac{905 + 906}{C_{00+}}, \ \omega_{p_2} = \frac{9m4 + 904 + 902}{C_2}, \ \omega_{p_3} = \frac{9m3 + 933 + 990}{C_3}$$

$$e, \frac{i_{0,1}}{at} = \left(i_{0,1}^{2} + i_{0,3}^{2} + i_{0,5}^{2} + i_{0,5}^{2} + i_{0,5}^{2} + i_{0,5}^{2} + i_{0,5}^{2}\right) 2$$

$$=\frac{16}{3}kT\left[\left(g_{n_{1}}+g_{m_{2}}\right)\left(\frac{1}{901+903+9m3}\right)^{2}g_{m_{5}}^{2}+9m_{5}^{2}+9m_{5}^{2}+\left(9m_{2}+9m_{4}\right)\left(\frac{1}{902}+904+9m_{4}\right)^{2}g_{m_{5}}^{2}+g_{m_{5}}^{2$$

$$f, \quad V_{i,n} = \frac{i_{o,n}}{\delta F} \left(\frac{1}{Gn} \right)^{2}$$

Scratch Paper